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August 22, 2003

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Commissioner for Patents
PO Box 1450
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Re: U.S. Utility Patent Application
Appl. No. 10/020,208; Filed: December 18, 2001
For: **System and Method to Screen Defect Related
Reliability Failures in CMOS SRAMS**
Inventors: *Bhattacharya et al.*
Our Ref: 1875.0330001

Sir:

Transmitted herewith for appropriate action are the following documents:

1. SKGF Cover Letter; and
2. Amendment and Reply under 37 C.F.R. § 1.111.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned. The U.S. Patent and Trademark

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Commissioner for Patents
August 22, 2003
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Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

A handwritten signature in black ink, appearing to read "Donald J. Featherstone", written in a cursive style.

Donald J. Featherstone
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DJF/MDS:mlb
Enclosures

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